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UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **NAKAMURA, Shunji**

Attention: **Applications Division**

Serial Number: **10/003,304**

Group Art Unit: **2811**

Filed: **December 6, 2001**

**FOR: SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR FABRICATING  
THE SAME**

P.T.O. Confirmation No.: **7007**

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
Washington, D.C. 20231

March 28, 2002

Sir:

Please supply the undersigned attorney with a corrected filing receipt for the above-identified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the official Filing Receipt, we noted an error in that the filing date is incorrect ("01/17/2002" should read as - -12/6/01- -). A copy of the Application Transmittal is enclosed which indicates the correct information. We are enclosing a copy of the filing receipt with the corrections highlighted.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully Submitted,  
ARMSTRONG, WESTERMAN & HATTORI, LLP

Sadao Kinashi

Attorney for Applicant

Reg. No. 48, 075

SK /srb

Atty. Docket No. **960456B**  
Suite 1000, 1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



**23850**

PATENT TRADEMARK OFFICE

Enclosures: Copy of Official Filing Receipt with Highlighted Corrections; Copy of Application Transmittal

SK

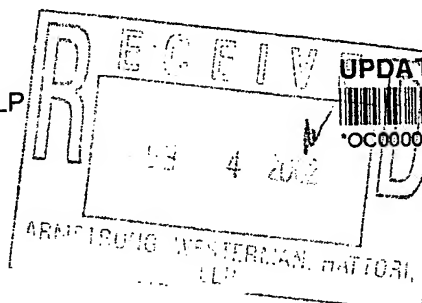


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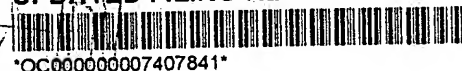
APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/003,304	<del>01/17/2002</del> 12/6/01	2811	992	960456B	39	16	6

23850  
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CONFIRMATION NO. 7007

UPDATED FILING RECEIPT



\*OC00000007407841\*

Date Mailed: 02/01/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

Shunji Nakamura, Kawasaki-shi, JAPAN;

**Assignment For Published Patent Application**

FUJITSU LIMITED, Kawasaki, JAPAN;

**Domestic Priority data as claimed by applicant**

THIS APPLICATION IS A DIV OF 08/970,212 11/14/1997

**Foreign Applications**

JAPAN 07-137968/1995 06/05/1995

If Required, Foreign Filing License Granted 12/19/2001

Projected Publication Date: 05/16/2002

Non-Publication Request: No

Early Publication Request: No

**Title**

Semiconductor memory device and method for fabricating the same

Preliminary Class

257

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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